



CMOS Circuit Design, Layout, and Simulation, Revised Second Edition

R. Jacob Baker

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Winner of the Frederick Emmons Terman Award, "CMOS: Circuit Design, Layout, and Simulation, Revised, Second Edition" covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. The features include: updated materials to reflect CMOS technology's movement into nanometer sizes; discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise; more than 1,000 figures, 200 examples, and over 500 end-of-chapter problems; in-depth coverage of both analog and digital circuit-level design techniques; and real-world process parameters and design rules. The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning.

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